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PATENT

P6495a

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Yoshiaki Mori, et al. Group Art Unit: 1796

Serial No.: 10/026,286 Examiner: Not Yet Assigned

Filed: December 28, 2001

Title: Pattern Forming Method And Apparatus Used For Semiconductor Device, Electric Circuit, Display Module, And Light Emitting Device

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on this date.

Date: June 27, 2002-

Mary Bastida

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Preliminary to examination please amend the above identified application as follows:

IN THE DRAWINGS:

Applicants submit proposed drawing changes to Figures 2, 5, 18 to 29 and 33 to 34 indicated in red on the attached sheets.

The corrections to Fig. 2 adds reference numeral 128 to make it consistent with the text of the specification.

The correction to Fig. 5 changes reference numeral "200" to "300" to maintain a proper antecedent basis with its description in the specification.

The corrections to Figs. 18 to 29 and Figs. 33 to 34 remove their respective hyphen and add parenthesis around the last number(s) in order to maintain consistency with the text of the specification. For example, "Fig. 18-1" is changed to "Fig. 18 (1)".

Figs. 33 to 34 additionally add the legend "Prior Art" to maintain consistency with their description in the specification.